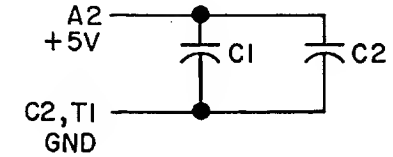
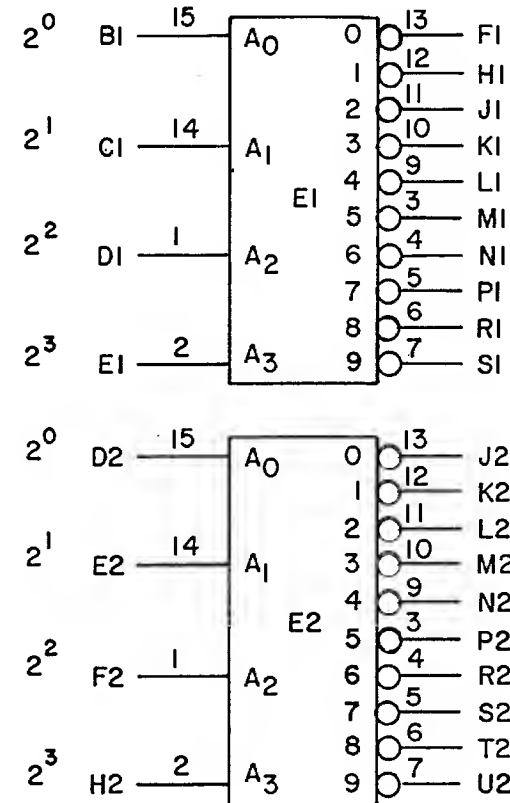


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UNLESS OTHERWISE INDICATED:
PIN 8 ON EACH IC = GND
PIN 16 ON EACH IC = +5V
E1 AND E2 ARE FAIRCHILD 9301
CAPACITORS ARE .01 MFD

REVISIONS	CHK	CHG NO.	REV.	A	DRN. BUTLER CHK'D ENG. A. Chas. Butler PROD.	DATE 1/29/69 DATE 2/2/69 DATE 6/17/69 DATE	TRANSISTOR & DIODE CONVERSION CHART				digital EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE DUAL BINARY TO DECIMAL DECODER M163			
	DEC	EIA	DEC	EIA			SIZE B	CODE CS	NUMBER M163-0-1	REV. A					
							PRINTED CIRCUIT REV. A								